REMARKS

Claims 1-6 are pending in this application. In the Office Action, the Examiner has rejected Claims 1-6 under 35 U.S.C. §102(e) as being anticipated by U.S. Patent No. 6,185,411 B1 (Gillig).

Regarding the Examiner's rejection of independent Claim 1 under 35 U.S.C. §102(e) as being anticipated by Gillig, Claim 1 has been amended.

Gillig teaches a PLL being formed from a plurality of elements. Each of these elements has a different response time. Gillig further teaches enabling a first element having a first response time according to a first control signal input of a loop bandwidth adjuster 301 and thereafter operating a second element having a response time shorter than that of the first element if a second control signal is generated. The outputs of the various elements are shown in FIG. 4. In other words, Gillig teaches a PLL wherein only select elements of the PLL are enabled and that other elements of the PLL are disabled (for example, the phase detector 202 is not enabled and does not produce a phase error signal at line 207 during warm-up of the PLL) so that the PLL can lock in less time than was previously known.

Contrary to that which is taught by Gillig, Claim 1, as amended, recites a radio transmitter portion for receiving the transmission local oscillation signal, a radio reception portion for receiving the reception local oscillation signal and a controller configured to control the radio transmitter portion to operate only during a transmission burst period and to control

the radio reception portion to operate only during a reception burst period, which is neither taught nor suggested by Gillig. Accordingly, it is respectfully requested that the rejection under 35 U.S.C. §102(e) of Claim 1 be withdrawn.
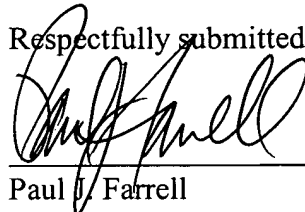
Regarding the Examiner's rejection of independent Claims 2, 3 and 5 under 35 U.S.C. §102(e), Claims 2, 3 and 5 have been amended. Claim 2, as amended, recites a radio transmitter portion for receiving the transmission local oscillation signal, a radio reception portion for receiving the reception local oscillation signal, and a controller for controlling the first PLL block to operate before an end point of a reception burst period, for controlling the second PLL block to operate before an end point of a transmission burst period, for controlling the radio transmitter portion to operate only during a transmission burst period and for controlling the radio reception portion to operate only during a reception burst period; Claim 3, as amended, recites controlling a radio transmitter portion to operate only during a transmission burst period, and controlling a radio reception portion to operate only during a reception burst period; and Claim 5, as amended, recites controlling a radio transmitter portion to operate only during a transmission burst period, controlling the second PLL block to operate before the end point of a transmission burst period, and controlling a radio reception portion to operate only during a reception burst period; which are neither taught nor suggested by Gillig. Accordingly, in light of amendments to Claims 2, 3 and 5 and the discussion above with respect to Claim 1, it is respectfully requested that the rejection under 35 U.S.C. §102(e) of Claims 2, 3 and 5 be withdrawn.

In light of the discussion above, it is respectfully submitted that independent Claims 1-3

and 5 overcome the stated rejections. Without conceding the patentability per se of dependent Claims 4 and 6 it is respectfully submitted that these claims also overcome the rejections by virtue of their dependence on Claims 3 and 5, respectively. Claims 1-6 are believed to be in condition for allowance.

Should the Examiner believe that a telephone conference or personal interview would facilitate resolution of any remaining matters, the Examiner may contact Applicants' attorney at the number given below.

Respectfully submitted,

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